AMENDMENTS TO THE SPECIFICATION:

Please replace paragraphs at page 1, lines 13-19 with the following amended paragraphs:

-- C. Visweswariah, "System and Method for Probabilistic Criticality Prediction of Digital Circuits," Docket number YOR92003-0402US1, U.S. Patent Application Number (to be assigned) filed on Sep. 19, 2003 7,086,023, issued August 1, 2006.

C. Visweswariah, "System and Method for Incremental Statistical Timing Analysis of Digital Circuits," Docket number YOR92003-0401, U.S. Patent Application Number (to be assigned) filed on 9/18/03 7,111,260, issued Sep. 19, 2006. --